CDS 130-003 Computing for Scientists

Midterm Exam Review

Oct. 21, 2010

Topics

Session 1: Fundamentals

CH1: Binary Representation

- Binary to decimal conversion: power method
- Decimal to binary conversion: long division method

CH2: Data Storage

- Data density
- Bits and bytes
- ASCII table, encoding

CH3: Logic Circuits and Logic Tables

- Transistor: the building block
- AND gate, AND table; OR gate, OR table; NOT gate, NOT table
- NAND gate, NAND table
- XOR (EOR) gate, XOR table
- Adding binary number

Session 2: Measurements

CH1: Sensors

- Electric measurement
- CCD (Charge Coupled Device)

CH2: Sensor Limitations

- Lower limit and upper limit
- Resolutions: Time, Space, and Level
- ADC: Analog to Digital Converter
- Signal, noise and background
- Signal to Noise Ration (SNR)

Session 3: Scientific Simulation

CH1: Mathematical model

- Scientific model to mathematic model
- CH2: Numerical method
 - Iteration
 - Differential equation
 - Interval and sub-interval
 - Integration
 - Integration accuracy and size of sub-interval (or number of iteration)
- CH3: Verification and Validation
 - Scientific method: characterization, hypothesis, prediction and testing

Sample Midterm Exam (2010 Oct)

This is a close-book exam. Calculator is allowed. Your answer should be given in the exam papers immediately following the questions.

1. Binary-to-decimal Convert 00111 to decimal?

2. Decimal-to-binary Convert 49 to binary?

3. ASCII table

What is the decimal number of character "B" in ASCII table? What is the binary number of "B"?

4. Logic Gates and Logic Tables

In the image below, four NANDS are connected and three of the inputs are set to 1. What are the values of Z if B = 1 and A = 0? For reference, the logic table associated with a NAND gate is shown.

- A. Z=0, output=0
- B. Z=0, output=1
- C. Z=1, output=0
- D: Z=1, output=1
- E: can not be determined



5. Sensor: Analog to Digital Converter

In a 7 bit analog to digital converter, how many different levels of data can be recorded?

- A. 7
- B. 70
- C. 128
- D. 256
- E. 1024

6. Sensor: resolution

A scientific temperature sensors has an operational range between -200 and 200 degree in Fahrenheit, and use 12 bits to convert the data. What is the temperature resolution of the detector?

7. Iteration Considering the following iteration code, what is A(12)?

> A(9)=13; for i=[10:12] A(i)=A(i-1)+37; end

The following statement is used for question 8, 9, 10, 11

My bank offers 5% interest on my account compounded yearly. Every year I deposit an additional \$100. Assuming that on the first year your account had a balance of \$100.

8. Scientific Model to Mathematical Model Translate the above statement into a mathematical representation

9. Mathematical Model to Computational Model Translate your mathematical representation into Matlab/Octave code that uses iteration to specify your balance after 10 years.

10. Interval and sub-interval When you are asked to calculate the balance for the next 10 years on a yearly basis. What is the interval and sub-interval of the calculation?

11. What is the balance in the 4th year?

12. Differentiation equation

Translate the following differential equation into the iteration mathematical representation

$$\frac{dP}{dt} = 0.5 \times P$$

13. Integration

$$f(x) = x$$
$$F = \int_{0}^{4.0} x dx$$

Consider the integration of the function, f(x)=x, from the interval of x=0 to x=4.0. You are asked to make a numerical approximation of this integration using the area method, that is to find the geometric area underneath the function with the sum of a series of rectangles. If you choose the sub-interval x=1.0, what is the approximated area?

14. If the sub-interval is chosen to be x=0.1, do you think that you will get a different value of the numerical integration? Explain why?

15. Validation: scientific method Describe in a few sentences in your word what is a scientific method?

Information Sheet

1. ASCII Table

Dec	H	Oct	Char		Dec	Hx	Oct	Html	Chr	Dec	Hx	Oct	Html	Chr	Dec	Hx	Oct	Html Cl	hr
0	0	000	NUL	(null)	32	20	040	6#32;	Space	64	40	100	@	0	96	60	140	& #96;	2
1	1	001	SOH	(start of heading)	33	21	041	!	1	65	41	101	G#65;	A	97	61	141	& # 97;	a
2	2	002	STX	(start of text)	34	22	042	6#34;		66	42	102	s#66;	в	98	62	142	b	b
3	3	003	ETX	(end of text)	35	23	043	#	#	67	43	103	C	С	99	63	143	& # 99;	С
4	4	004	EOT	(end of transmission)	36	24	044	\$	ş	68	44	104	D	D	100	64	144	d	d
5	5	005	ENQ	(enquiry)	37	25	045	%	*	69	45	105	E	Е	101	65	145	e	e
6	6	006	ACK	(acknowledge)	38	26	046	&	6	70	46	106	F	F	102	66	146	f	f
7	7	007	BEL	(bell)	39	27	047	'	1 I	71	47	107	6#71;	G	103	67	147	g	g
8	8	010	BS	(backspace)	40	28	050	((72	48	110	6#72;	н	104	68	150	h	h
9	9	011	TAB	(horizontal tab)	41	29	051	<i>&</i> #41;)	73	49	111	¢#73;	I	105	69	151	i	i
10	À	012	LF	(NL line feed, new line)	42	2A	052	*	*	-74	4A	112	s#74;	J	106	6A	152	j	Ĵ
11	В	013	VT	(vertical tab)	43	2B	053	6#43;	+	75	4B	113	¢#75;	K	107	6B	153	k	k
12	С	014	FF	(NP form feed, new page)	44	2C	054	«#44;	1	76	4C	114	s#76;	L	108	6C	154	l	1
13	D	015	CR	(carriage return)	45	2D	055	&# 45 ;	- 1	77	4D	115	6 #77;	М	109	6D	155	m	m
14	Е	016	S0	(shift out)	46	2 E	056	&#46;</td><td>\cdot</td><td>78</td><td>4E</td><td>116</td><td>N</td><td>N</td><td>110</td><td>6E</td><td>156</td><td>n</td><td>n</td></tr><tr><td>15</td><td>F</td><td>017</td><td>SI</td><td>(shift in)</td><td>47</td><td>2F</td><td>057</td><td>/</td><td>1</td><td>79</td><td>4F</td><td>117</td><td>O</td><td>0</td><td>111</td><td>6F</td><td>157</td><td>o</td><td>0</td></tr><tr><td>16</td><td>10</td><td>020</td><td>DLE</td><td>(data link escape)</td><td>48</td><td>30</td><td>060</td><td>0;</td><td>0</td><td>80</td><td>50</td><td>120</td><td>P</td><td>Р</td><td>112</td><td>70</td><td>160</td><td>p</td><td>p</td></tr><tr><td>17</td><td>11</td><td>021</td><td>DC1</td><td>(device control 1)</td><td>49</td><td>31</td><td>061</td><td>6#49;</td><td>1</td><td>81</td><td>51</td><td>121</td><td>G#81;</td><td>Q</td><td>113</td><td>71</td><td>161</td><td>G#113;</td><td>q</td></tr><tr><td>18</td><td>12</td><td>022</td><td>DC2</td><td>(device control 2)</td><td>50</td><td>32</td><td>062</td><td>2</td><td>2</td><td>82</td><td>52</td><td>122</td><td>R</td><td>R</td><td>114</td><td>72</td><td>162</td><td>r</td><td>r</td></tr><tr><td>19</td><td>13</td><td>023</td><td>DC3</td><td>(device control 3)</td><td>51</td><td>33</td><td>063</td><td>3</td><td>3</td><td>83</td><td>53</td><td>123</td><td>S</td><td>S</td><td>115</td><td>73</td><td>163</td><td>s</td><td>S</td></tr><tr><td>20</td><td>14</td><td>024</td><td>DC4</td><td>(device control 4)</td><td>52</td><td>34</td><td>064</td><td>4</td><td>4</td><td>84</td><td>54</td><td>124</td><td>¢#84;</td><td>Т</td><td>116</td><td>74</td><td>164</td><td>t</td><td>t</td></tr><tr><td>21</td><td>15</td><td>025</td><td>NAK</td><td>(negative acknowledge)</td><td>53</td><td>35</td><td>065</td><td>5</td><td>5</td><td>85</td><td>55</td><td>125</td><td>s#85;</td><td>U</td><td>117</td><td>75</td><td>165</td><td>u</td><td>u</td></tr><tr><td>22</td><td>16</td><td>026</td><td>SYN</td><td>(synchronous idle)</td><td>54</td><td>36</td><td>066</td><td>«#54;</td><td>6</td><td>86</td><td>56</td><td>126</td><td>V</td><td>V</td><td>118</td><td>76</td><td>166</td><td>v</td><td>v</td></tr><tr><td>23</td><td>17</td><td>027</td><td>ETB</td><td>(end of trans. block)</td><td>55</td><td>37</td><td>067</td><td>7</td><td>7</td><td>87</td><td>57</td><td>127</td><td>⊊#87;</td><td>M</td><td>119</td><td>77</td><td>167</td><td>w</td><td>W</td></tr><tr><td>24</td><td>18</td><td>030</td><td>CAN</td><td>(cancel)</td><td>56</td><td>38</td><td>070</td><td>8</td><td>8</td><td>88</td><td>58</td><td>130</td><td>¢#88;</td><td>X</td><td>120</td><td>78</td><td>170</td><td>⊛#120;</td><td>x</td></tr><tr><td>25</td><td>19</td><td>031</td><td>EM</td><td>(end of medium)</td><td>57</td><td>39</td><td>071</td><td>9</td><td>9</td><td>89</td><td>59</td><td>131</td><td>s#89;</td><td>Y</td><td>121</td><td>79</td><td>171</td><td>y</td><td>У</td></tr><tr><td>26</td><td>1A</td><td>032</td><td>SUB</td><td>(substitute)</td><td>58</td><td>ЗA</td><td>072</td><td>:</td><td>:</td><td>90</td><td>5A</td><td>132</td><td>¢#90;</td><td>Z</td><td>122</td><td>7A</td><td>172</td><td>&#122;</td><td>Z</td></tr><tr><td>27</td><td>1B</td><td>033</td><td>ESC</td><td>(escape)</td><td>59</td><td>3B</td><td>073</td><td>;</td><td>2</td><td>91</td><td>5B</td><td>133</td><td>[</td><td>E</td><td>123</td><td>7B</td><td>173</td><td>{</td><td>{</td></tr><tr><td>28</td><td>10</td><td>034</td><td>FS</td><td>(file separator)</td><td>60</td><td>3C</td><td>074</td><td>«#6O;</td><td><</td><td>92</td><td>5C</td><td>134</td><td>\</td><td>1</td><td>124</td><td>7C</td><td>174</td><td> </td><td>1</td></tr><tr><td>29</td><td>1D</td><td>035</td><td>GS</td><td>(group separator)</td><td>61</td><td>3D</td><td>075</td><td>=</td><td>=</td><td>93</td><td>5D</td><td>135</td><td>6#93;</td><td>1</td><td>125</td><td>7D</td><td>175</td><td>}</td><td>}</td></tr><tr><td>30</td><td>1E</td><td>036</td><td>RS</td><td>(record separator)</td><td>62</td><td>3E</td><td>076</td><td>></td><td>></td><td>94</td><td>5E</td><td>136</td><td>6#94;</td><td>~</td><td>126</td><td>7E</td><td>176</td><td>~</td><td>~</td></tr><tr><td>31</td><td>lF</td><td>037</td><td>US</td><td>(unit separator)</td><td>63</td><td>ЗF</td><td>077</td><td>?</td><td>2</td><td>95</td><td>5F</td><td>137</td><td>_</td><td>_</td><td>127</td><td>7F</td><td>177</td><td></td><td>DEL</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>5</td><td>ourc</td><td>6: 4</td><td>ww.</td><td>Look</td><td>upTables</td><td>s.com</td></tr></tbody></table>											

2. NAND gate and its logic table

NAND gate



Input A	Input B	Output
0	0	1
0	1	1
1	0	1
1	1	0